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1. An imaging device for capturing optical image data, the device comprising:
 - an imager for generating an image signal;
 - a memory component that receives the image signal from the imager and stores the image signal as image data; and
 - a processor that executes an exposure control routine by implementing a module that controls the exposure and gain setting in the imager and a second module that implements computations in response to image data transmitted from the memory component and exposure data transmitted from the first module to determine a desired exposure and gain setting.

2. The imaging device of Claim 1, wherein the imager generates the image signal from multi-dimensional symbologies.

3. The imaging device of Claim 1, wherein the processor provides the imaging device with multi-tasking capabilities.

20 one application program of the imaging device.

5. The imaging device of Claim 1, wherein the processor executes an operating system of the imaging device.

6. The imaging device of Claim 1, wherein the processor executes at least one application program and an operating system of the imaging device.

25 7. The imaging device of Claim 1, wherein the first module is implemented
in a high priority thread.

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8. The imaging device of Claim 1, wherein the first module is implemented in a high priority task.
9. The imaging device of Claim 1, wherein the first module is implemented in an interrupt service routine.
- 5 10. The imaging device of Claim 1, wherein the second module is implemented in a low priority thread routine.
11. The imaging device of Claim 1, wherein the second module is implemented in a low priority task routine.
- 10 12. The imaging device of Claim 1, wherein the second module comprises histogram processing.
13. The imaging device of Claim 1, wherein the first module is implemented in a an interrupt service routine and the second module is implemented in a low priority task routine.
- 15 14. The imaging device of Claim 1, further comprising a Direct Memory Access (DMA) controller that receives the image signals from the imager, responds to an image capture command from the second module and transfers captured image signals into the memory component.
- 15 15. The imaging device of Claim 14, wherein the processor comprises the DMA controller.
- 20 16. The imaging device of Claim 1, further comprising a programmable logic device that serves as an interface between the imager and the processor.
17. The imaging device of Claim 16, wherein the programmable logic device comprises a DMA controller that receives the image signals from the imager, responds to an image capture command from the second module and transfers captured image signals into the memory module.

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18. An imaging device for capturing optical image data, the device comprising:
- an imager for generating an image signal;
- a memory component that receives the image signal from the imager and stores the image signal as image data; and
- a processor that implements a high priority module for real time control of the imager and a lower priority module that examines the image signal and provides feedback to the high priority module routine.
19. A method for exposure control in a multi-dimensional imaging device, the method comprising:
- generating, at an imager, an end of frame signal;
- executing, at a central processor, a first module that controls exposure and gain settings in the imager in response to the end of frame signal;
- generating, in the first module, a captured contrast setting, wherein contrast is defined as the product of the exposure setting and the gain setting;
- executing, at the central processor, a second module that calculates a target contrast setting in response to the end of frame signal, the captured contrast setting and stored image data;
- generating, in the first module, a subsequent exposure and gain setting for the imager in response to the target contrast setting; and
- implementing the subsequent exposure and gain setting in the imager.
20. The method of Claim 19, wherein the central processor is additionally responsible for executing at least one imaging device application program.
21. The method of Claim 19, wherein the central processor is additionally responsible for executing an image device operating system.
22. The method of Claim 19, wherein the central processor is additionally responsible for executing at least one imaging device application program and an imaging device operating system.

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23. The method of Claim 19, wherein executing, at a central processor, a first module that controls exposure and gain settings in the imager in response to the image signal further comprises executing the first module in a high priority thread routine.

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24. The method of Claim 19, wherein executing, at a central processor, a first module that controls exposure and gain settings in the imager in response to the image signal further comprises executing the first module in a high priority task routine.

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25. The method of Claim 19, wherein executing, at a central processor, a first module that controls exposure and gain settings in the imager in response to the image signal further comprises executing the first module in a interrupt service routine.

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26. The method of Claim 19, wherein executing, at the central processor, a second module that calculates a target contrast setting in response to the image signal and the captured contrast setting further comprises executing the second module in a low priority thread routine.

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27. The method of Claim 19, wherein executing, at the central processor, a second module that calculates a target contrast setting in response to the image signal and the captured contrast setting further comprises executing the second module in a low priority task routine.

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28. The method of Claim 19, wherein executing, at the central processor, a second module that calculates a target contrast setting in response to the end of frame signal and the captured contrast setting further comprises implementing histogram processing to calculate a target contrast setting.

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29. A program storage device readable by a processor, tangibly embodying a program of instructions executable by the processor to perform a method for exposure control in a multi-dimensional imaging, the method comprising:

5 generating, in a high priority module, a captured contrast setting in response to an end of frame signal from an imager, wherein contrast is defined as the product of exposure setting and gain setting;

10 calculating, in a low priority module, a target contrast setting in response to the end of frame signal from the imager, the captured contrast setting and stored image data;

15 generating, in the high priority module, a subsequent exposure and gain setting for the imager in response to the target contrast setting; and

20 implementing the subsequent exposure and gain setting in an imager of the multi-dimensional imaging device.

15 30. The program storage device of Claim 29, wherein generating, in a high priority module, a captured contrast setting in response to an end of frame signal from an imager and generating, in the high priority module, a subsequent exposure and gain setting for the imager in response to the target contrast setting further comprises generating in an interrupt service routine module.

20 31. The program storage device of Claim 29, wherein generating, in a high priority module, a captured contrast setting in response to an end of frame signal from an imager and generating, in the high priority module, a subsequent exposure and gain setting for the imager in response to the target contrast setting further comprises generating in a high priority thread module.

25 32. The program storage device of Claim 29, wherein generating, in a high priority module, a captured contrast setting in response to an end of frame signal from an imager and generating, in the high priority module, a subsequent exposure and gain

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setting for the imager in response to the target contrast setting further comprises generating in a high priority task module.

33. The program storage device of Claim 29, wherein calculating, in a low priority module, a target contrast setting in response to the end of frame image signal from the imager, the captured contrast setting and stored image data further comprises calculating in a low priority task module.

10 34. The program storage device of Claim 29, wherein calculating, in a low priority module, a target contrast setting in response to the end of frame image signal from the imager, the captured contrast setting and stored image data further comprises calculating in a low priority thread module.

15 35. The program storage device of Claim 29, wherein calculating, in a low priority module, a target contrast setting in response to the end of frame image signal from the imager, the captured contrast setting and stored image data further comprises implementing histogram processing to calculate a target contrast setting.

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